



## Ethernet Trunk Management Software

### IEEE 1588 Module

#### Key Features

- Implements the Precision Time Protocol (PTP)
- Support for Ordinary Clocks, Boundary Clocks, and Transparent Clocks
- Can operate as a Grandmaster, Master, Slave or Passive device
- Integrates with NComm SSM TMS product for full system timing management
- Fully Standard Compliant
- Pre-ported to Linux (version 2.4 and 2.6)
- OS independent
- MIB support
- Easily customizable to hardware environments (e.g. for time stamping)

#### Key Benefits

- Turnkey solution
- Easy to use APIs
- Sample application included
- ANSI C Source Code
- Driver Included

With NComm's proven source code and protocol stack, you have the quality and standard compliance interfaces that you need for less cost than you can do it yourself.

#### Product Overview

NComm's Ethernet TMS puts the IEEE 1588 functionality within the reach of every equipment manufacturer.

Ethernet TMS handles the Precision Time Protocol requirements defined by the IEEE 1588 standard. In addition, the IEEE 1588 timing source can act as a source of timing for NComm Synchronization Status Message (SSM) TMS product. The SSM TMS manages the timing distribution for a system from sources including IEEE 1588 as well as T1, E1, E3, and SONET/SDH.

Ethernet TMS includes the higher level, managed object MIB-style of control and status methodology to properly manage the IEEE 1588 product.

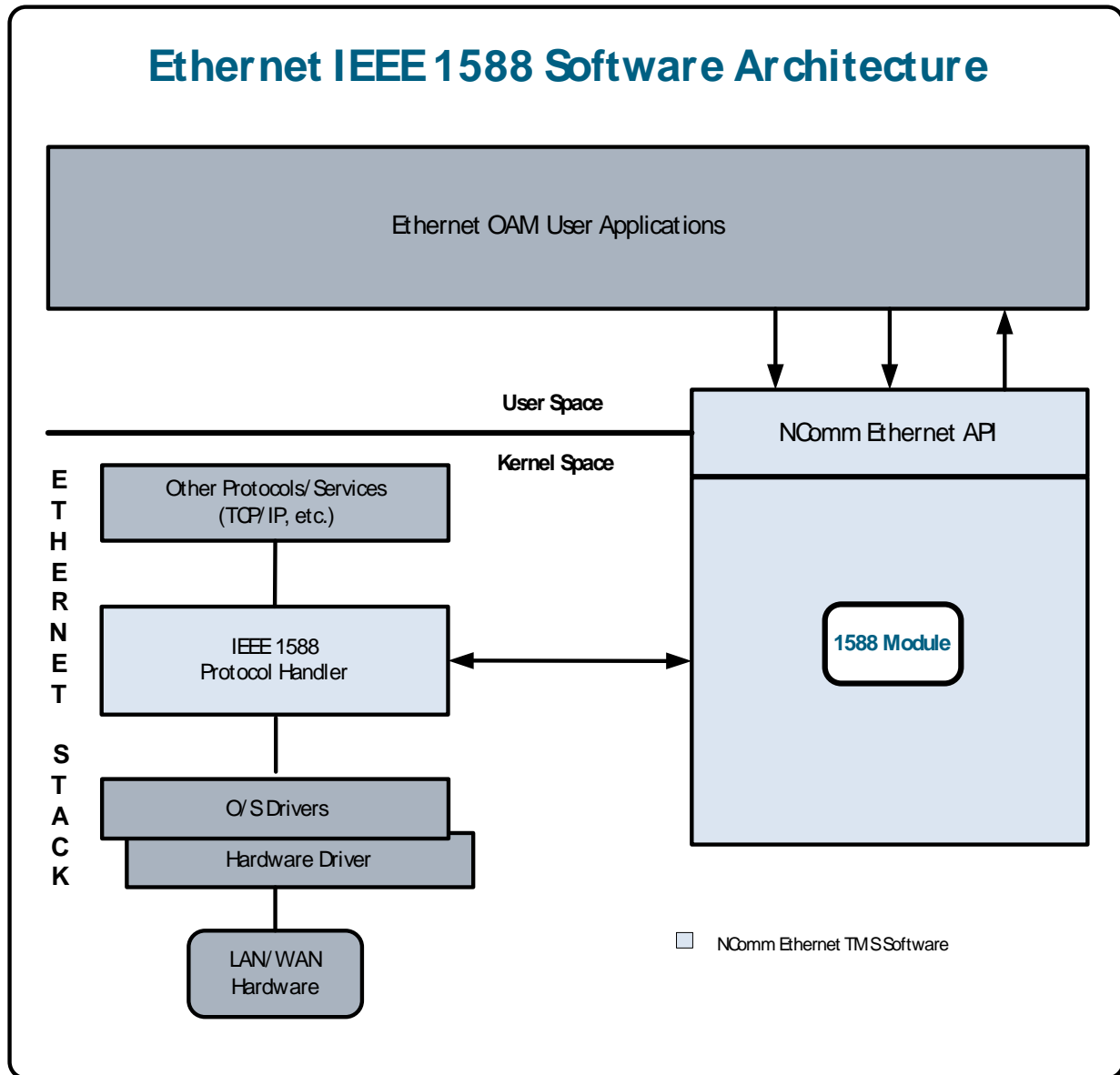
NComm's Ethernet TMS is supplied as ANSI C source code. User manuals, implementation training and technical support are also included with each license. A sample demo application provides functionality very quickly. This sample application also functions as a guide for integration of the Ethernet OAM TMS API into the upper management or control systems of your choice.

#### Applications

- Routers
- Switches
- Base Stations
- Access Points
- Aggregation devices
- Test Equipment
- Central Office Switches

## Ethernet TMS Architecture

As in the entire TMS family of software, Ethernet TMS is architected to be hardware and operating system independent. Well-defined APIs are employed for faster first time integration and ease of reuse.



### Driver and IEEE 1588 Software Architecture

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